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**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

10/30/01
10000440
10/30/01

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10000440	10/30/2001	324	538	2358	JAT

**APPLICANTS: Johnson Gerald; 286

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
Verified and Acknowledged Examiners's initials		<i>[Signature]</i>	1495-US
TITLE : Control loop compensation circuit and method			

U.S.DEP'T. OF COMM./PAT & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Figs.Drwg.
				Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
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